

FOLDING ANALOG TO DIGITAL CONVERTER CAPABLE OF CALIBRATION AND METHOD THEREOF

Abstract

An ADC is disclosed comprising at least one folder receiving an input voltage, generating a first output voltage and a second output voltage, and including a plurality of amplifiers and calibration logic for generating bias control signals according to the first output voltage and the second output voltage. Each of the amplifier receives one of a plurality of reference voltages and comprises a bias circuit for providing a bias current to the amplifier based on at least one of a plurality of bias control signals. During calibration, the calibration logic provides the bias control signals to control the bias circuit of each amplifier in the folder such that the first output voltage is substantially the same as the second output voltage.